Day : Friday Date: 11/7/2003



PALM INTRANET

Time: 15:11:01

Inventor Name Search Result

Your Search was:

Last Name = PARK

First Name = YOUNG-HOON

| Application# | Patent# | Status | Date Filed | Title | Inventor Name 23 |
|--------------|------------------|--------|------------|--|----------------------|
| 10464340 | Not Issued | 030 | | IMAGE SENSOR AND METHOD OF FABRICATING THE SAME | PARK, YOUNG- HOON |
| 10461265 | Not Issued | 030 | 06/13/2003 | CMOS IMAGE SENSOR AND METHOD OF FABRICATING THE SAME | PARK, YOUNG- HOON |
| 10396902 | Not Issued | 093 | | INTEGRATED CIRCUIT CHIP HAVING ANTI- MOISTURE-ABSORPTION FILM AT EDGE THEREOF AND METHOD OF FORMING ANTI- MOISTURE-ABSORPTION FILM | PARK, YOUNG- HOON |
| 10322455 | Not Issued | 041 | 12/19/2002 | MICROORGANISM OVERPRODUCING 5'- XANTHYLIC ACID | PARK, YOUNG- HOON |
| 10199538 | Not Issued | 030 | 07/19/2002 | FUSE AREA STRUCTURE INCLUDING PROTECTION FILM ON SIDEWALL OF FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME | PARK, YOUNG- HOON |
| 10102312 | 65 <u>631</u> 62 | 150 | | SEMICONDUCTOR MEMORY DEVICE FOR | PARK, YOUNG- HOON |

| | | | | REDUCING PARASITIC BIT LINE CAPACITANCE AND METHOD OF FABRICATING THE SAME | |
|----------|---------------|-----|------------|---|----------------------|
| 10094571 | 6573184 | 150 | 03/07/2002 | APPARATUS AND METHOD FOR DEPOSITING THIN FILM ON WAFER USING ATOMIC LAYER DEPOSITION | PARK, YOUNG- HOON |
| 09971778 | 6555450 | 150 | 10/04/2001 | CONTACT FORMING METHOD FOR SEMICONDUCTOR DEVICE | PARK, YOUNG- HOON |
| 09935971 | 6509255 | 150 | 08/23/2001 | FUSE AREA STRUCTURE HAVING GUARD RING SURROUNDING FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME | PARK, YOUNG- HOON |
| 09885432 | 6423589 | 150 | 06/20/2001 | METHODS FOR FABRICATING CMOS INTEGRATED CIRCUITS INCLUDING SOURCE/DRAIN COMPENSATING REGIONS | PARK, YOUNG- HOON |
| 09848579 | 6579372 | 150 | 05/03/2001 | APPARATUS AND METHOD FOR DEPOSITING THIN FILM ON WAFER USING ATOMIC LAYER DEPOSITION | PARK, YOUNG- HOON |
| 09848577 | Not Issued | 041 | 05/03/2001 | REACTOR FOR DEPOSITING THIN FILM ON WAFER | PARK, YOUNG- HOON |
| 09848533 | Not Issued | 071 | 05/03/2001 | ATOMIC LAYER DEPOSITION (ALD) THIN FILM DEPOSITION EQUIPMENT HAVING CLEANING APPARATUS | PARK, YOUNG- HOON |

| ī. | | | | | |
|-----------------|----------|-----|------------|--|----------------------|
| | <u> </u> | L | | AND CLEANING METHOD | |
| 09826590 | 6465895 | 150 | 04/05/2001 | BONDING PAD STRUCTURES FOR SEMICONDUCTOR DEVICES AND FABRICATION METHODS THEREOF | PARK, YOUNG- HOON |
| 09739186 | 6448113 | 150 | 12/18/2000 | FUSE AREA STRUCTURE INCLUDING PROTECTION FILM ON SIDEWALL OF FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME | PARK, YOUNG- HOON |
| 09719593 | 6616754 | 150 | 12/12/2000 | SEGREGATION REDUCING AGENT CONSISTING OF CURDLAN AND ALKALINE SOLUTION AND HYDRAULIC COMPOSITION CONTAINING THE SEGREGATION REDUCING AGENT | PARK, YOUNG- HOON |
| <u>09715679</u> | 6507086 | 150 | 11/17/2000 | FUSE AREA STRUCTURE HAVING GUARD RING SURROUNDING FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME | PARK, YOUNG- HOON |
| 09715372 | 6566735 | 150 | 11/17/2000 | INTEGRATED CIRCUIT CHIP HAVING ANTI- MOISTURE-ABSORPTION FILM AT EDGE THEREOF AND METHOD OF FORMING ANTI- MOISTURE-ABSORPTION FILM | PARK, YOUNG- HOON |
| 09710953 | 6509263 | 150 | 11/14/2000 | METHOD FOR FABRICATING A SEMICONDUCTOR MEMORY DEVICE | PARK, YOUNG- HOON |

| | | | | HAVING POLYSILICON WITH AN ENHANCED SURFACE CONCENTRATION AND REDUCED CONTACT RESISTANCE | |
|----------|---------|-----|------------|---|----------------------|
| 09479049 | 6176906 | 150 | 01/07/2000 | QUATERNARY AMMONIUM SALT FOR PHOTO-CURABLE ANTIFOGGING COMPOSITION, METHOD FOR PREPARING THE SAME, AND PHOTO- CURABLE ANTIFOGGING COMPOSITION | PARK, YOUNG- HOON |
| 09088784 | 6096925 | 150 | 06/01/1998 | QUATERNARY AMMONIUM SALT FOR PHOTO-CURABLE ANTIFOGGING COMPOSITION, METHOD FOR PREPARING THE SAME, AND PHOTO- CURABLE ANTIFOGGING COMPOSITION | PARK, YOUNG-HOON |
| 08855024 | 6274914 | 150 | 05/13/1997 | CMOS INTEGRATED CIRCUITS INCLUDING SOURCE/DRAIN PLUG | PARK, YOUNG-HOON |
| 08440954 | 5545578 | 150 | 05/15/1995 | METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE HAVING A LOW- RESISTANCE GATE ELECTRODE | PARK , YOUNG-HOON |

Inventor Search Completed: No Records to Display.

| | Last Name | First Name |
|--------------------------|-----------|-------------------|
| Search Another: Inventor | PARK | YOUNG-HOON Search |

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page